

DS - 06/23/2005  
Substitute for Form 1449 A & B/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	Unassigned 10/540513
Confirmation Number	Unassigned
Filing Date	June 23, 2005
First Named Inventor	Takenori OSADA
Art Unit	Unassigned 2926
Examiner Name	Unassigned Eduardo Rodela
Attorney Docket Number	Q88662

Sheet 1 of 1

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code <sup>2</sup> (if known)		
		US			
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
ER		JP	06-84959	A	03-25-1994	Fujitsu Ltd.	
ER		JP	06-163599	A	06-10-1994	NEC Corp.	
ER		JP	2001-210819	A	08-03-2001	Hitachi Cable, Ltd.	

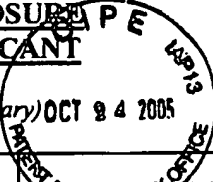
**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
ER		U. Strauße et al., "Carrier mobilities in graded In <sub>x</sub> Ga <sub>1-x</sub> As/Al <sub>0.2</sub> Ga <sub>0.8</sub> As quantum wells for high electron mobility transistors", <i>Journal of Applied Physics</i> , Vol. 80, No. 1, July 1, 1996, pp. 322-325.	

Examiner Signature	/Eduardo Rodela/ EAR	Date Considered	05/05/2006
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to indicate here if English language Translation is attached.

Substitute for Form 1449 A & B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Complete if Known	
			Application Number	10/540,513
			Confirmation Number	Unassigned
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			First Named Inventor	Takenori OSADA
			Art Unit	Unassigned 2826
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ER		JP	6-84959	A	03-25-1994	Fujitsu Ltd.	Abstract
ER		JP	6-163599	A	06-10-1994	NEC Corp.	Abstract
ER		JP	2001-210819	A	08-03-2001	Hitachi Cable Ltd.	Abstract
ER		JP	6-21106	A	01-28-1994	Hitachi Cable, Ltd.	Abstract
ER		JP	2708863	B2	10-17-1997	Sanyo Electric Co., Ltd.	Abstract

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ER		U. Strauss et al., "Carrier mobilities in graded In <sub>x</sub> Ga <sub>1-x</sub> As/Al <sub>0.2</sub> Ga <sub>0.8</sub> As quantum wells for high electron mobility transistors", <i>J. Appl. Phys.</i> , Vol. 80, No. 1, July 1, 1996, pp. 322-325	
ER		J.W. Matthews et al., "Defects in Epitaxial Multilayers", <i>Journal of Crystal Growth</i> , Vol. 27, 1974, pp. 118-125	
ER		J.W. Matthews et al., "Defects in Epitaxial Multilayers", <i>Journal of Crystal Growth</i> , Vol. 32, No. 2, 1976, pp. 265-273	

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ER		Yohei Otoki, "Compound Semiconductor Materials for Electronic Devices", <i>The Semiconductor Industry News Forum: "Compound Semiconductor, Materials Head into Mass Production Stages"</i> , June 5, 2002, pp. 1, 7-8, with English Abstract	

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